

East Central Railway

Office of the  
General Manager/S&T  
Hajipur

No. ECR/S&T/Corres/Con/Div/2023 (Computer No. 5369)

Date: 08/04/2024.

CSTE/Works  
CSTE/Con/South & North/MHX  
GM/IRCON/PNBE, GM/RVNL/PNBE  
Sr.DSTE/DDU/DHN/DNR/SEE & SPJ

**Sub:- Regard utilization of input/output bits during FAT.**

On 16/03/2024, IBH was commissioned between Athmalgola (ATL) and Barh (BARH) stations of DNR division by Works unit. During commissioning of the system, it was noticed that during FAT, clearance of Up Adv of RH with IBS at YY (as it was distant signal of ATL Home also) was not tested. It was tested for clearance with R/G of IBS only. Because of this, logic was to be modified during NI and the commissioning got delayed affecting train operations severely.

Accordingly, all units are advised to demand all the input and output bits for simulation during FAT testing so that such discrepancies can be averted in future.

राकेश रंजन  
(राकेश रंजन) 08/04/24  
मुख्य सिगनल इंजीनियर-1

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