



एकनाथ मोहकर
प्रधान मुख्य सिगनल एवं दूरसंचार इंजीनियर
Eknath Mohker

Principal Chief Signal & Telecom Engineer



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ECR-HQ/S&T/Policy

17th Oct 2023

ED/S&T/C.O./RVNL
New Delhi

CGM/S&T/Co-ord/IRCON
New Delhi

Sub : Computer based FAT(AutoFAT) on EI logic.

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It has been observed that during manual FAT on EI logic, several negative testings (unwanted proving) remain un-tested and FAT takes more time in identifying & rectifying errors.

In order to enhance safety by getting the EI Logic tested through AutoFAT for ST/LT testing, route release with long & short train, EWN operation only with concerned track down, square sheet testing and negative testing, etc. it is advised to get the computer based AutoFAT done by competent agencies having IRSE/IRSTELO license holder designers before start of manual FAT by RVNL/IRCON/Railway Officials.

Besides AutoFAT on EI Logic, EI Logic verification may be got done from IRSE/IRSTELO license holder designers after completion of manual FAT by concerned agencies.

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EKNATH MOHKER
Date: 2023.10.17
16:22:48 +05'30'
(Eknath Mohker)
PCSTE/ECR

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CSTE/C/N, CSTE/C/S & CSTE/Wks/HQ -

For information & necessary action in all future works please.