East Central Railway

No. ECR/S&T/DRG/Corres/2024

Office of GM (S&T)/HJP Date-24.12.2024

CSTE/CON/North & South, GM/S&T/RVNL/PATNA & RANCHI GM (S&T) IRCON, Patna Sr.DSTE/DNR, DDU, SEE, DHN & SPJ, DY.CSTE/Works/DNR, DDU & DHN, DY.CSTE/Project/HJP & DY.CSTE/RE,

Sub:- Typical circuit diagram of mid section interlocked L.C.Gate in single line.

Typical circuit diagram of mid section interlocked L.C.Gate in single line section has been prepared superseding previous circuit diagram of mid section interlocked L.C. Gate in single line ECR/TYP/LC/96/2024, dated: 29.05.24.This circuit diagram of L.C.Gate in single line has been approved by PCSTE/ECR/HJP for implementation over ECR.

This is for information and implementation over ECR with immediate effect.

DA: Approved circuit diagram of L.C.Gate in single line.

(Rahul Priyadarsy)
Dy. CSTE/D&D/HJP
for GM/S&T/ECR

TYPICAL CIRCUIT DIAGRAM OF L.C GATE IN SINGLE LINE



























